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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/783,481	02/20/2004	2818	1172	5649-1235	7	28	6

CONFIRMATION NO. 1306

20792
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 PO BOX 37428
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CORRECTED FILING RECEIPT



OC000000012889871

Date Mailed: 06/07/2004

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Applicant(s)

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Domestic Priority data as claimed by applicant**Foreign Applications**

REPUBLIC OF KOREA 2003-36748 06/09/2003

If Required, Foreign Filing License Granted: 05/13/2004

Projected Publication Date: 12/09/2004

Non-Publication Request: No

Early Publication Request: No

Title

Memory devices having bit line precharge circuits with off current precharge control and associated bit line precharge methods

Preliminary Class

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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